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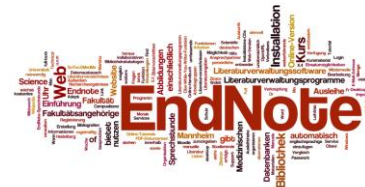
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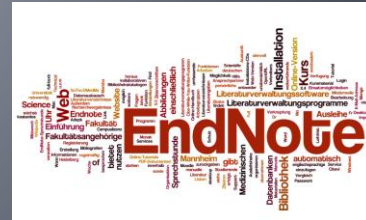
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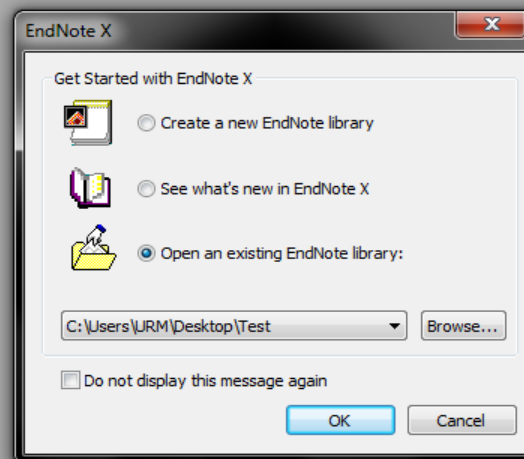
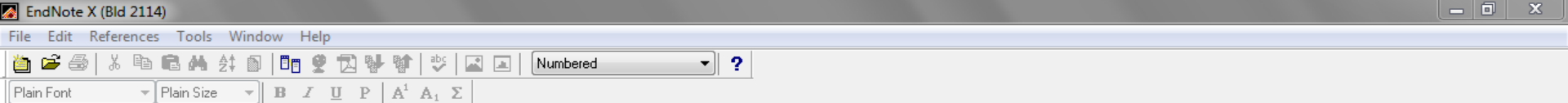
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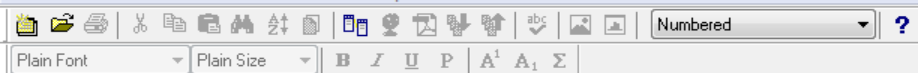
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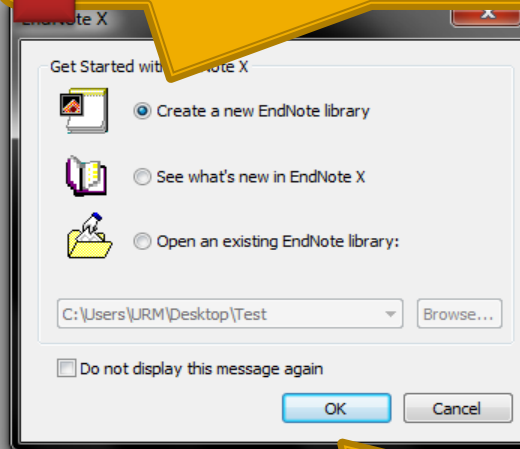
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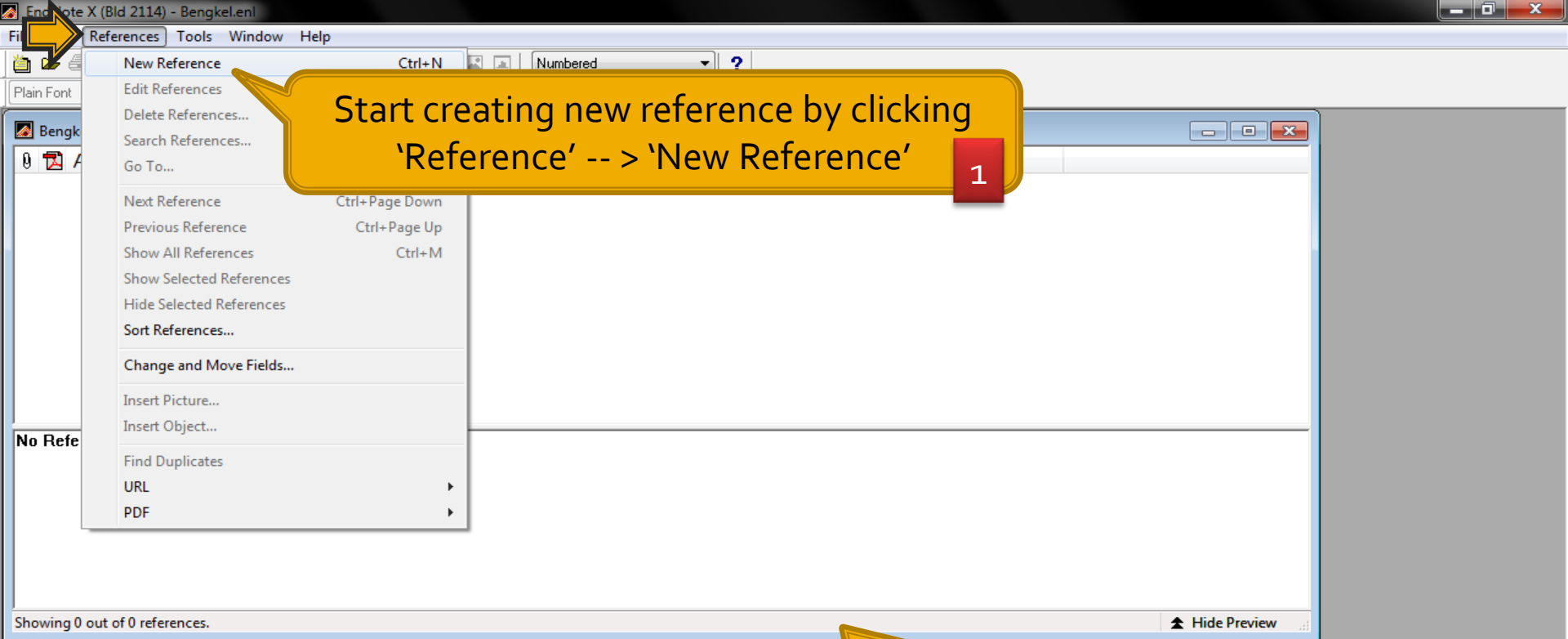
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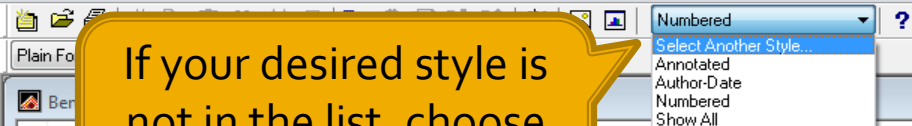
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2004

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26

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Pages
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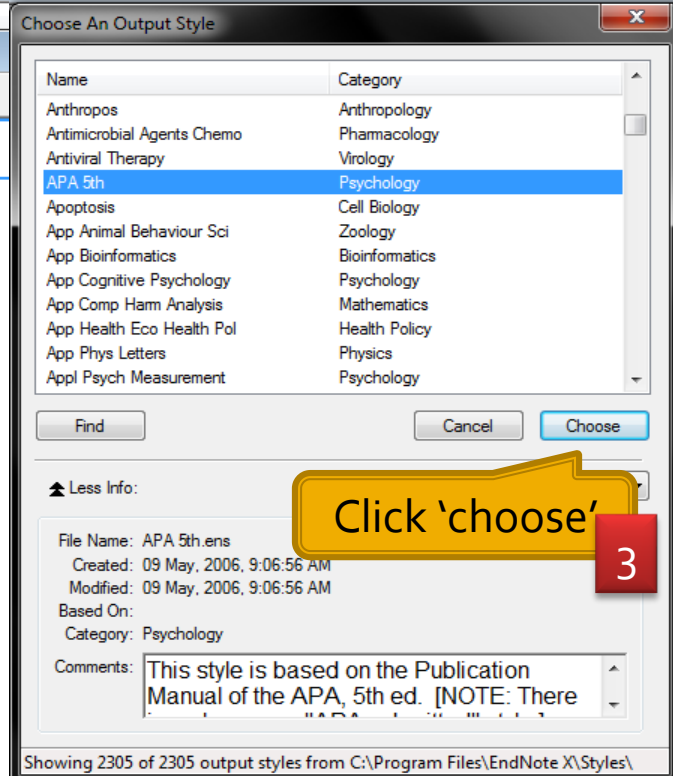
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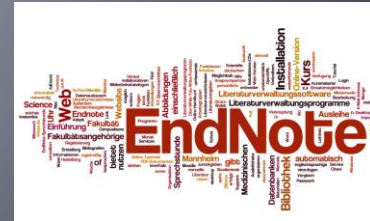
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► Technology acquired through joint design has the highest level. ► Technology acquired through purchasing production license has the lowest level. ► Technology acquired through domestic R&D has the level in between. ► A firm with related other businesses tends to have a higher level of technology. ► The influence of policies is significant for technology upgrade but not catch-up.

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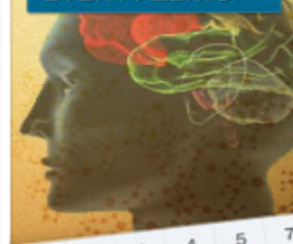
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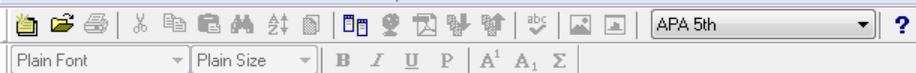
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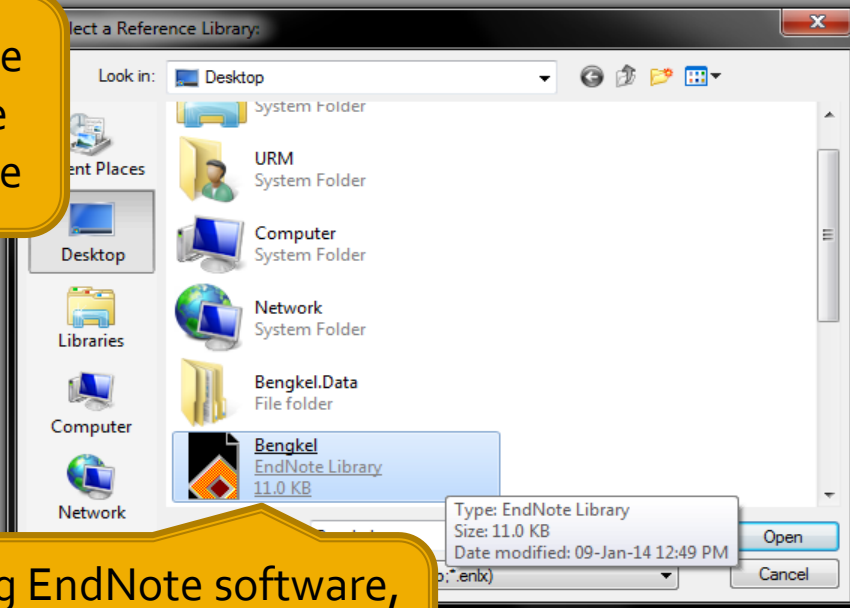


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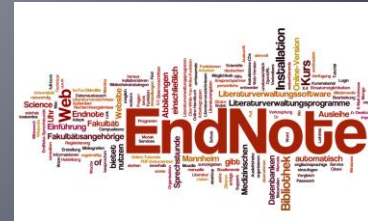
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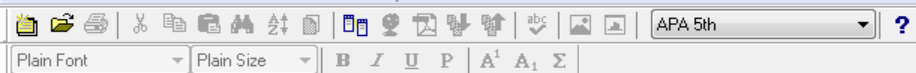
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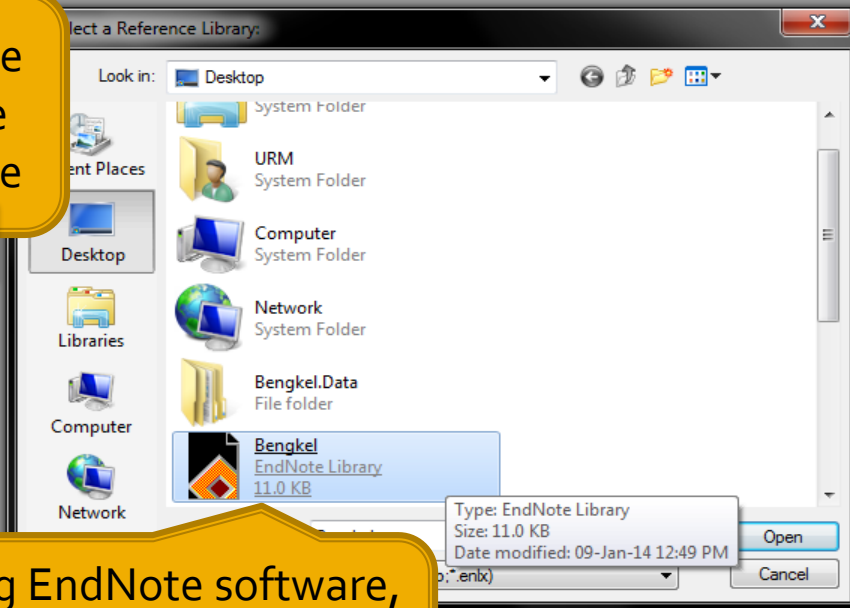


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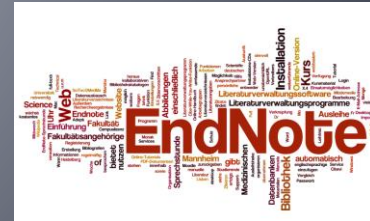
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Schlarman, M.E. ; Geiger, R.L.

Design of Mixed-Mode Integrated Circuits and Applications, 1999. Third International Workshop on

Digital Object Identifier: 10.1109/MMICA.1999.833596

Publication Year: 1999 , Page(s): 61 - 64

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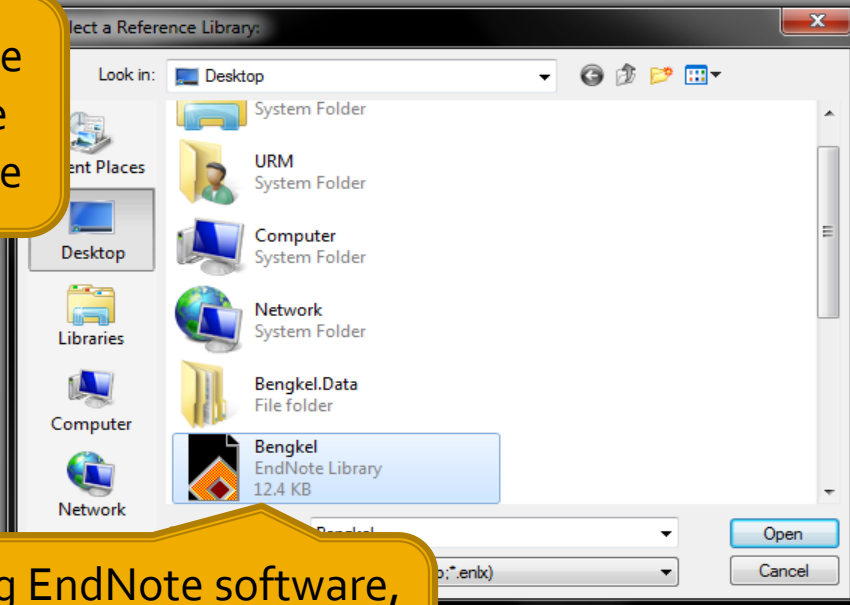
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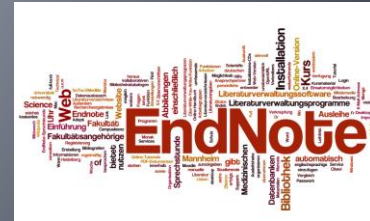
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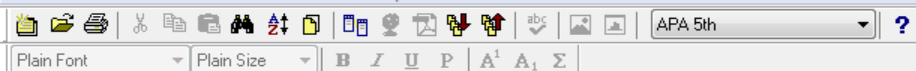
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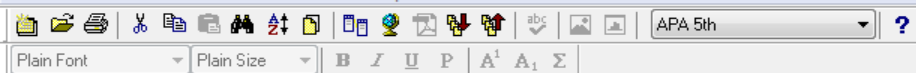
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ology expert elicitations: An application to natural gas turbine efficiencies. *Technological Forecasting and Social Change*(0).



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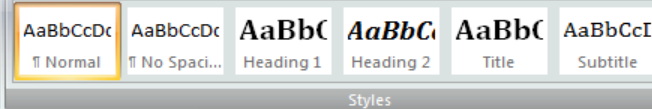
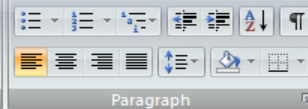
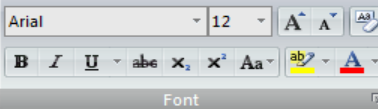
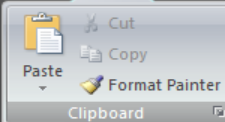
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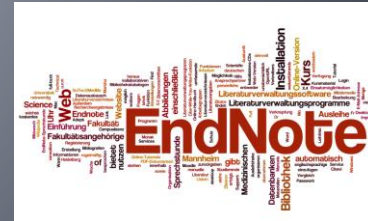


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- Qiu, Y., Ortolano, L., & David Wang, Y. Factors influencing the technology upgrading and catch-up of Chinese wind turbine manufacturers: Technology acquisition mechanisms and government policies. *Energy Policy*, 55(0), 305-316.
- Uhlmann, E., & Domingos, D. C. Development and Optimization of the Die-Sinking EDM-Technology for Machining the Nickel-based Alloy MAR-M247 for Turbine Components. *Procedia CIRP*, 6(0), 180-185.

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Design for Manufacturability and Yield - influence of process variations in digital, analog and mixed-signal circuit design

Chapter 1: Introduction

The concepts of design for manufacturability and design for yield DFM/DFY are bringing together domains that co-existed mostly separated until now. Circuit design, physical design and manufacturing process. New requirements like SoC, mixed analog/digital design and deep submicron technologies force us to think differently about design. With new deep-submicron technologies, the number of defects per chip is increasing. Random and systematic defects are becoming a major concern. On quality and yield of the design, the process technology, the on-chip variations, the design of the technologies larger than 180nm feature sizes, variations are mostly in a range of below 10%. Here an acceptable yield range is achieved by regular but error-prone re-shifts of the drifting process.

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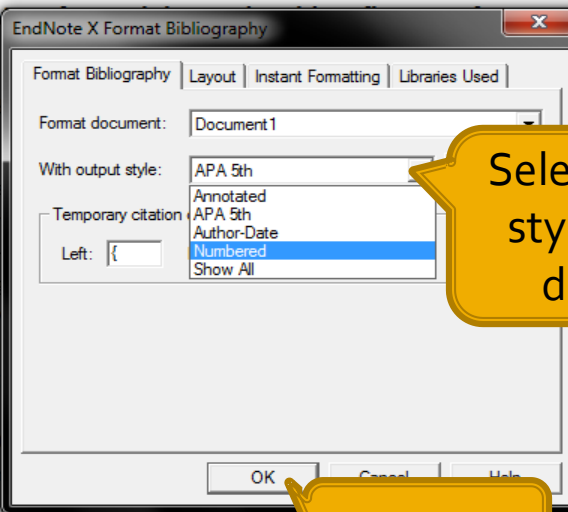


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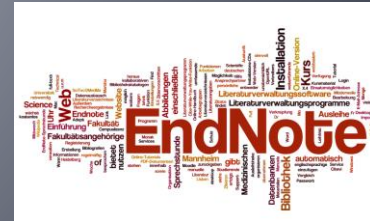
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